BCW65ALT1G, BCW65CLT1G

General Purpose Transistor NPN Silicon

Features

• These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	32	Vdc
Collector – Base Voltage	V _{CBO}	60	Vdc
Emitter – Base Voltage	V _{EBO}	5.0	Vdc
Collector Current – Continuous	Ι _C	800	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit	
Total Device Dissipation FR-5 Board (Note 1), $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	P _D	225 1.8	mW mW/°C	
Thermal Resistance, Junction–to–Ambient	R_{\thetaJA}	556	°C/W	
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C Derate above 25°C	P _D	300 2.4	mW mW/°C	
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	417	°C/W	
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

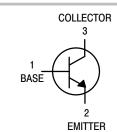
1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.

2. Alumina = $0.4 \times 0.3 \times 0.024$ in 99.5% alumina.



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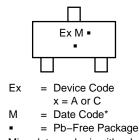
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CASE 318 STYLE 6

MARKING DIAGRAMS



(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
BCW65ALT1G	SOT-23 (Pb-Free)	3000/Tape & Reel
BCW65CLT1G	SOT-23 (Pb-Free)	3000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

BCW65ALT1G, BCW65CLT1G

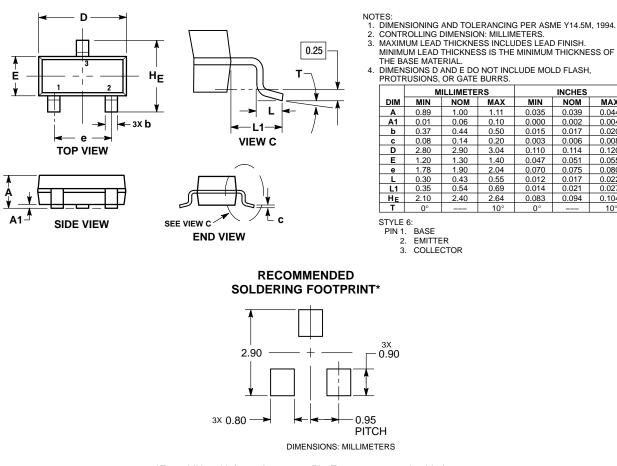
ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Мах	Unit
OFF CHARACTERISTICS	•	-	-	-	-
Collector – Emitter Breakdown Voltage $(I_C = 10 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	32	-	-	Vdc
Collector – Emitter Breakdown Voltage (I _C = 10 μ Adc, V _{EB} = 0)	V _{(BR)CES}	60	-	_	Vdc
Emitter – Base Breakdown Voltage ($I_E = 10 \ \mu Adc, I_C = 0$)	V _{(BR)EBO}	5.0	-	-	Vdc
Collector Cutoff Current ($V_{CE} = 32 \text{ Vdc}, I_E = 0$) ($V_{CE} = 32 \text{ Vdc}, I_E = 0, T_A = 150^{\circ}\text{C}$)	ICES			20 20	nAdc μAdc
Emitter Cutoff Current ($V_{EB} = 4.0 \text{ Vdc}, I_{C} = 0$)	I _{EBO}	-	-	20	nAdc
ON CHARACTERISTICS	•				-
$ \begin{array}{ll} \mbox{DC Current Gain} & \mbox{BCW65ALT1} \\ (I_C = 100 \ \mu \mbox{Adc}, \ V_{CE} = 10 \ \mbox{Vdc}) \\ (I_C = 10 \ \mbox{mAdc}, \ \ V_{CE} = 1.0 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	h _{FE}	35 75 100 35	- - -	- - 250 -	_
$\begin{array}{ll} \mbox{DC Current Gain} & \mbox{BCW65CLT1} \\ (I_C = 100 \ \mu \mbox{Adc}, \ V_{CE} = 10 \ \mbox{Vdc}) \\ (I_C = 10 \ \mbox{mAdc}, \ \ V_{CE} = 1.0 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	h _{FE}	80 180 250 100	- - - -	- - 630 -	_
Collector – Emitter Saturation Voltage ($I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc}$) ($I_C = 100 \text{ mAdc}, I_B = 10 \text{ mAdc}$)	V _{CE(sat)}		0.7 0.3		Vdc
Base – Emitter Saturation Voltage ($I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc}$)	V _{BE(sat)}	_	_	2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current-Gain — Bandwidth Product (I _C = 20 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)	fT	100	-	-	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	-	-	12	pF
Input Capacitance ($V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz}$)	C _{ibo}	_	-	80	pF
Noise Figure (V_{CE} = 5.0 Vdc, I _C = 0.2 mAdc, R _S = 1.0 kΩ, f = 1.0 kHz, BW = 200 Hz)	NF	-	-	10	dB
SWITCHING CHARACTERISTICS					
Turn–On Time ($I_{B1} = I_{B2} = 15 \text{ mAdc}$)	t _{on}	_	-	100	ns
Turn–Off Time (I _C = 150 mAdc, R _L = 150 Ω)	t _{off}	-	-	400	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AR**



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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