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FDMS0310AS

# N-Channel PowerTrench<sup>®</sup> SyncFET<sup>TM</sup> 30 V, 22 A, 4.3 m $\Omega$

#### **Features**

- Max  $r_{DS(on)} = 4.3 \text{ m}\Omega$  at  $V_{GS} = 10 \text{ V}$ ,  $I_D = 19 \text{ A}$
- Max  $r_{DS(on)} = 5.2 \text{ m}\Omega$  at  $V_{GS} = 4.5 \text{ V}$ ,  $I_D = 17 \text{ A}$
- Advanced package and Silicon combination for low r<sub>DS(on)</sub> and high efficiency
- SyncFET<sup>TM</sup> Schottky Body Diode
- MSL1 robust package design
- 100% UIL tested
- RoHS Compliant

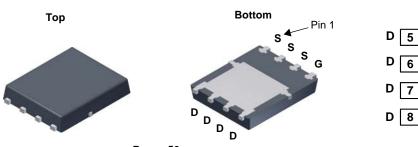


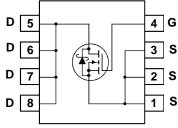
## **General Description**

The FDMS0310AS has been designed to minimize losses in power conversion application. Advancements in both silicon and package technologies have been combined to offer the lowest  $r_{\text{DS}(\text{on})}$  while maintaining excellent switching performance.This device has the added benefit of an efficient monolithic Schottky body diode.

# **Applications**

- Synchronous Rectifier for DC/DC Converters
- Notebook Vcore/GPU low side switch
- Networking Point of Load low side switch
- Telecom secondary side rectification





Power 56

## MOSFET Maximum Ratings T<sub>A</sub>= 25°C unless otherwise noted

Symbol	Parameter			Ratings	Units
$V_{DS}$	Drain to Source Voltage			30	V
$V_{GS}$	Gate to Source Voltage		(Note 4)	±20	V
	Drain Current -Continuous (Package limited)	T <sub>C</sub> = 25°C		22	
	-Continuous (Silicon limited)	T <sub>C</sub> = 25°C		80	A
<sup>I</sup> D	-Continuous	T <sub>A</sub> = 25°C	(Note 1a)	19	_ A
	-Pulsed			100	
E <sub>AS</sub>	Single Pulse Avalanche Energy		(Note 3)	33	mJ
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25°C		41	W
	Power Dissipation	T <sub>A</sub> = 25°C	(Note 1a)	2.5	VV
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Ra	ange		-55 to +150	°C

#### **Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance, Junction to Case		3.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	50	C/VV

#### **Package Marking and Ordering Information**

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS0310AS	FDMS0310AS	Power 56	13 "	12 mm	3000 units

# **Electrical Characteristics** T<sub>J</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	acteristics					
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$	30			V
BV <sub>DSST</sub>	Drain to Source Breakdown Voltage Transient	V <sub>GS</sub> = 0 V, Transient = 100 ns	33			V
ΔBV <sub>DS</sub> S ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 10 mA, referenced to 25°C		23		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V			500	μА
I <sub>GSS</sub>	Gate to Source Leakage Current, Forward	V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0 V			100	nA

#### **On Characteristics**

V <sub>GS(th)</sub>	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 1 \text{ mA}$	1.2	1.5	3.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I <sub>D</sub> = 10 mA, referenced to 25°C		-4		mV/°C
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 19 A		3.6	4.3	
r <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 17 A		4.5	5.2	mΩ
, ,		$V_{GS} = 10 \text{ V}, I_D = 19 \text{ A}, T_J = 125^{\circ}\text{C}$		4.8	6.0	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 19 A		103		S

#### **Dynamic Characteristics**

C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1MHz	1715	2280	pF
C <sub>oss</sub>	Output Capacitance		655	870	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		75	110	pF
$R_g$	Gate Resistance		0.7	2.5	Ω

### **Switching Characteristics**

t <sub>d(on)</sub>	Turn-On Delay Time		9.0	18	ns
t <sub>r</sub>	Rise Time	V <sub>DD</sub> = 15 V, I <sub>D</sub> = 19 A,	3.9	10	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$	25	40	ns
t <sub>f</sub>	Fall Time		3.2	10	ns
$Q_g$	Total Gate Charge	V <sub>GS</sub> = 0 V to 10 V	27	37	nC
Qg	Total Gate Charge	$V_{GS} = 0 \text{ V to } 4.5 \text{ V} V_{DD} = 15 \text{ V},$	13	19	nC
Q <sub>gs</sub>	Gate to Source Charge	I <sub>D</sub> = 19 A	4.2		nC
$Q_{gd}$	Gate to Drain "Miller" Charge		3.7		nC

#### **Drain-Source Diode Characteristics**

V <sub>SD</sub>	Source-Drain Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 2 \text{ A}$ (Note 2)	(	0.6	0.8	V
	Source-Drain blode Forward voltage	$V_{GS} = 0 \text{ V}, I_{S} = 19 \text{ A}$ (Note 2)	(	0.8	1.2	
t <sub>rr</sub>	Reverse Recovery Time	$I_E = 19 \text{ A, di/dt} = 300 \text{ A/µs}$		24	38	ns
Q <sub>rr</sub>	Reverse Recovery Charge	τ <sub>F</sub> = 19 A, αι/ατ = 300 Α/μς		24	38	nC

<sup>1</sup> R<sub>0JA</sub> is determined with the device mounted on a 1 in<sup>2</sup> pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R<sub>0JC</sub> is guaranteed by design while R<sub>0CA</sub> is determined by the user's board design.



a. 50 °C/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper.



b. 125 °C/W when mounted on a minimum pad of 2 oz copper.

<sup>2.</sup> Pulse Test: Pulse Width < 300  $\mu$ s, Duty cycle < 2.0%. 3. E<sub>AS</sub> of 33 mJ is based on starting T<sub>J</sub> = 25 °C, L = 0.3 mH, I<sub>AS</sub> = 15 A, V<sub>DD</sub> = 27 V, V<sub>GS</sub> = 10 V. 4. As an N-ch device, the negative Vgs rating is for low duty cycle pulse occurrence only. No continuous rating is implied.

# Typical Characteristics T<sub>J</sub> = 25°C unless otherwise noted

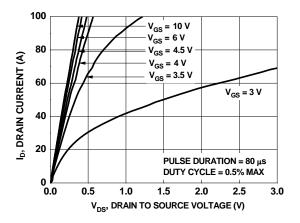


Figure 1. On Region Characteristics

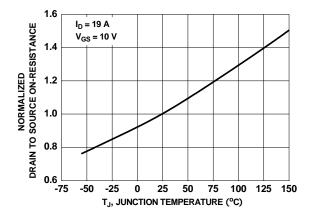


Figure 3. Normalized On Resistance vs Junction Temperature

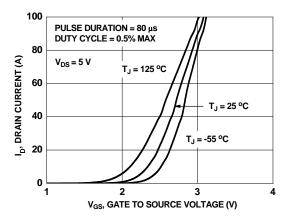


Figure 5. Transfer Characteristics

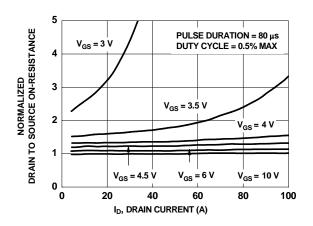


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

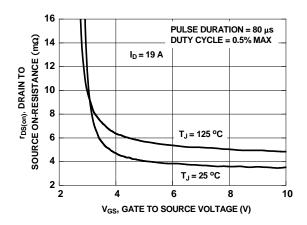


Figure 4. On-Resistance vs Gate to Source Voltage

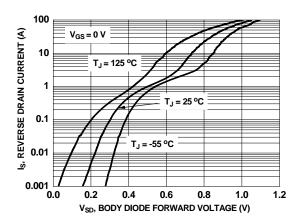


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

# Typical Characteristics T<sub>J</sub> = 25°C unless otherwise noted

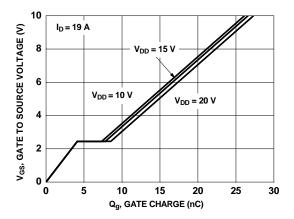


Figure 7. Gate Charge Characteristics

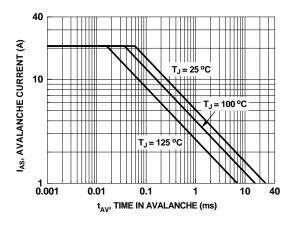


Figure 9. Unclamped Inductive Switching Capability

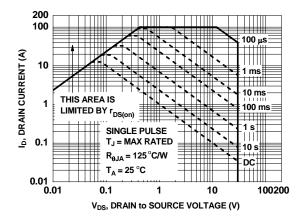


Figure 11. Forward Bias Safe Operating Area

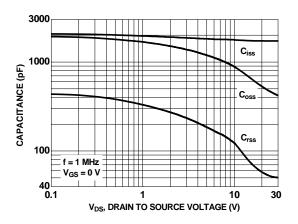


Figure 8. Capacitance vs Drain to Source Voltage

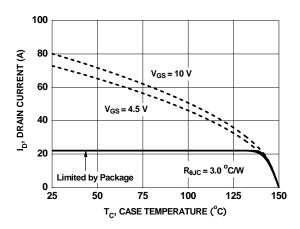


Figure 10. Maximum Continuous Drain Current vs Case Temperature

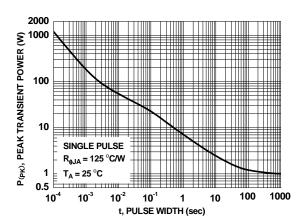


Figure 12. Single Pulse Maximum Power Dissipation

# **Typical Characteristics** T<sub>J</sub> = 25°C unless otherwise noted

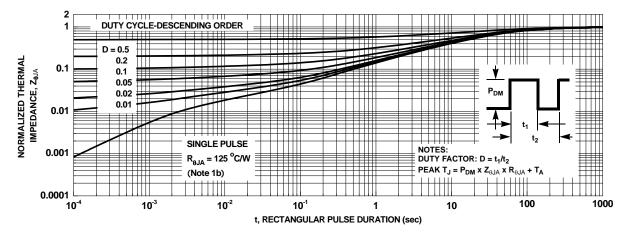


Figure 13. Junction-to-Ambient Transient Thermal Response Curve

# Typical Characteristics (continued)

# SyncFET<sup>TM</sup> Schottky body diode Characteristics

Fairchild's SyncFET<sup>TM</sup> process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 14 shows the reverse recovery characteristic of the FDMS0310AS.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

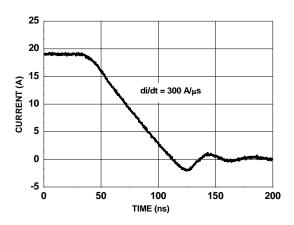


Figure 14. FDMS0310AS SyncFET<sup>TM</sup> body diode reverse recovery characteristic

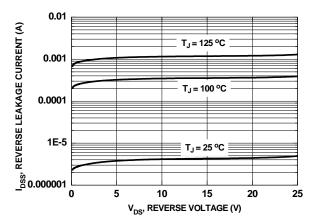
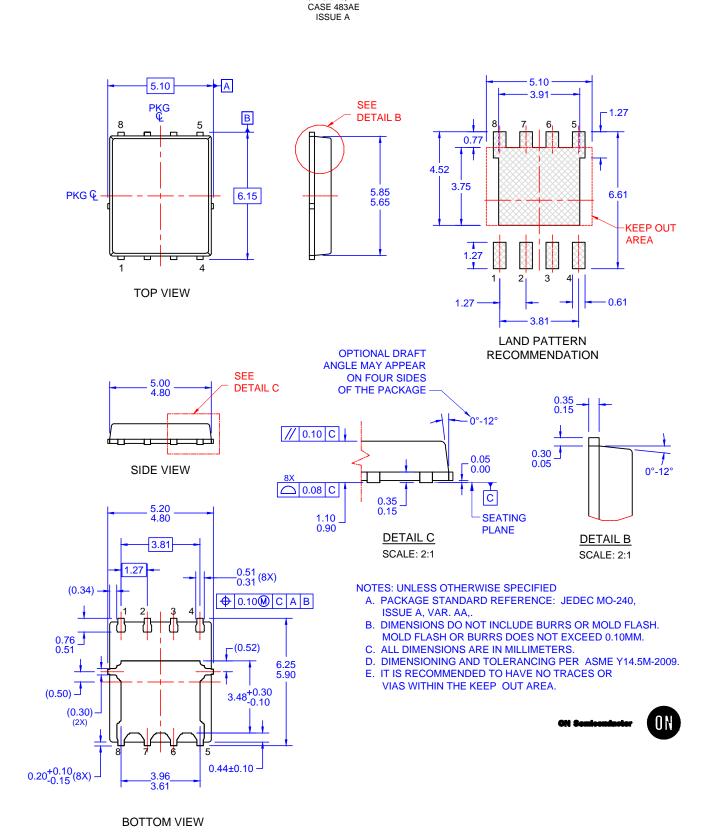


Figure 15. SyncFET<sup>TM</sup> body diode reverse leakage versus drain-source voltage



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